

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Sehat Sutardja Art Unit: 2826

Serial No.: 09/966,914 Examiner: Alexander Williams

Filed : September 27, 2001

Title : IMPROVED INTEGRATED CHIP PACKAGE HAVING INTERMEDIATE

SUBSTRATE

Commissioner for Patents Washington, D.C. 20231

RESPONSE

In response to the action mailed July 31, 2002, please TECHNOLOGY CENTER 2800 amend the application as follows:

In the Claims:

Claims 1, 3, 11-13, 15, 20, 22, 24, and 28 have been amended as follows:

(Twice Amended) An integrated chip package, comprising:

at least one semiconductor chip having a first surface and a second surface;

CERTIFICATE OF MAILING BY FIRST CLASS MAIL

I hereby certify under 37 CFR §1.8(a) that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated below and is addressed to the Commissioner for Patents, Washington, D.C. 20231.

Date of Deposit

Signature

Joseph L Stevenson

Typed or Printed Name of Person Signing Certificate